

Fig. 1 (PRIOR ART)

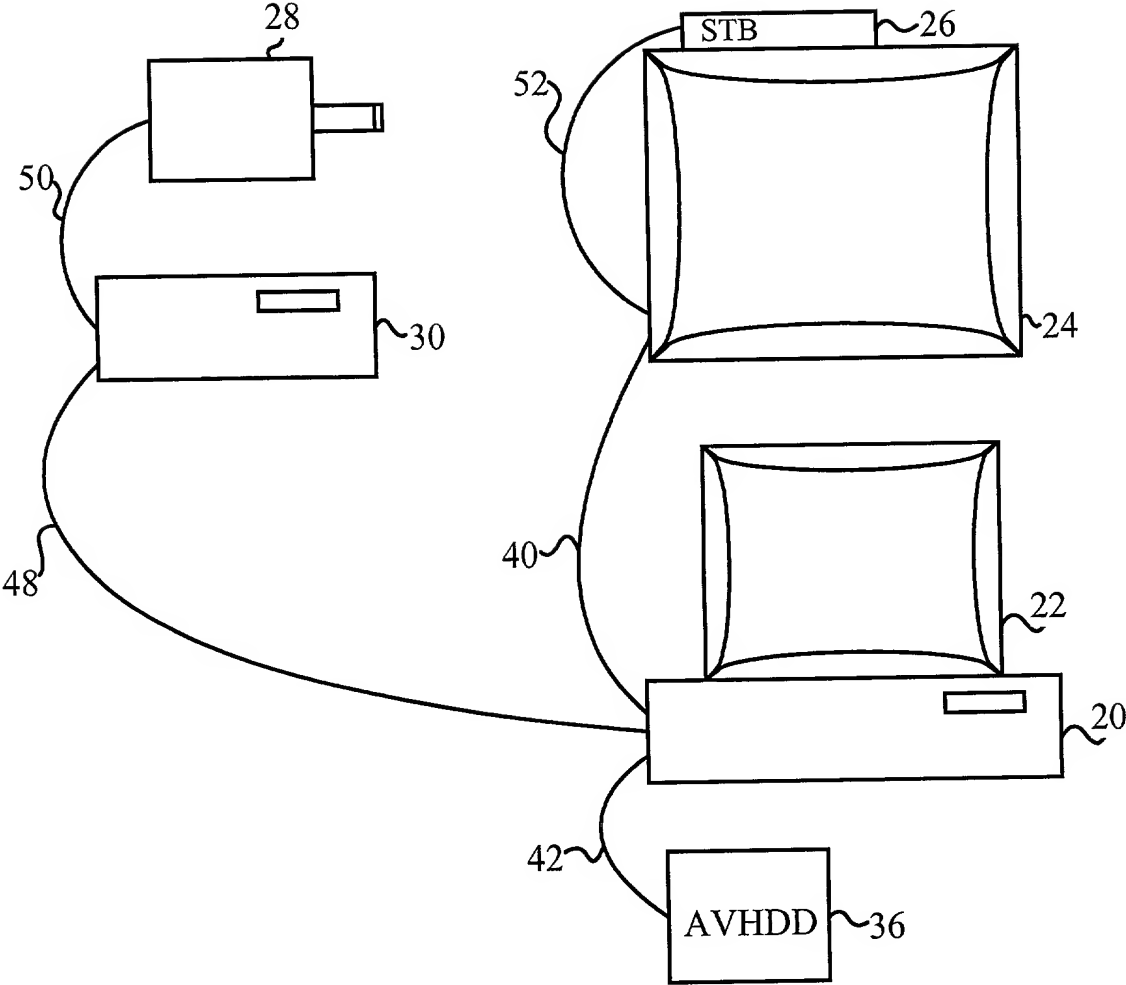
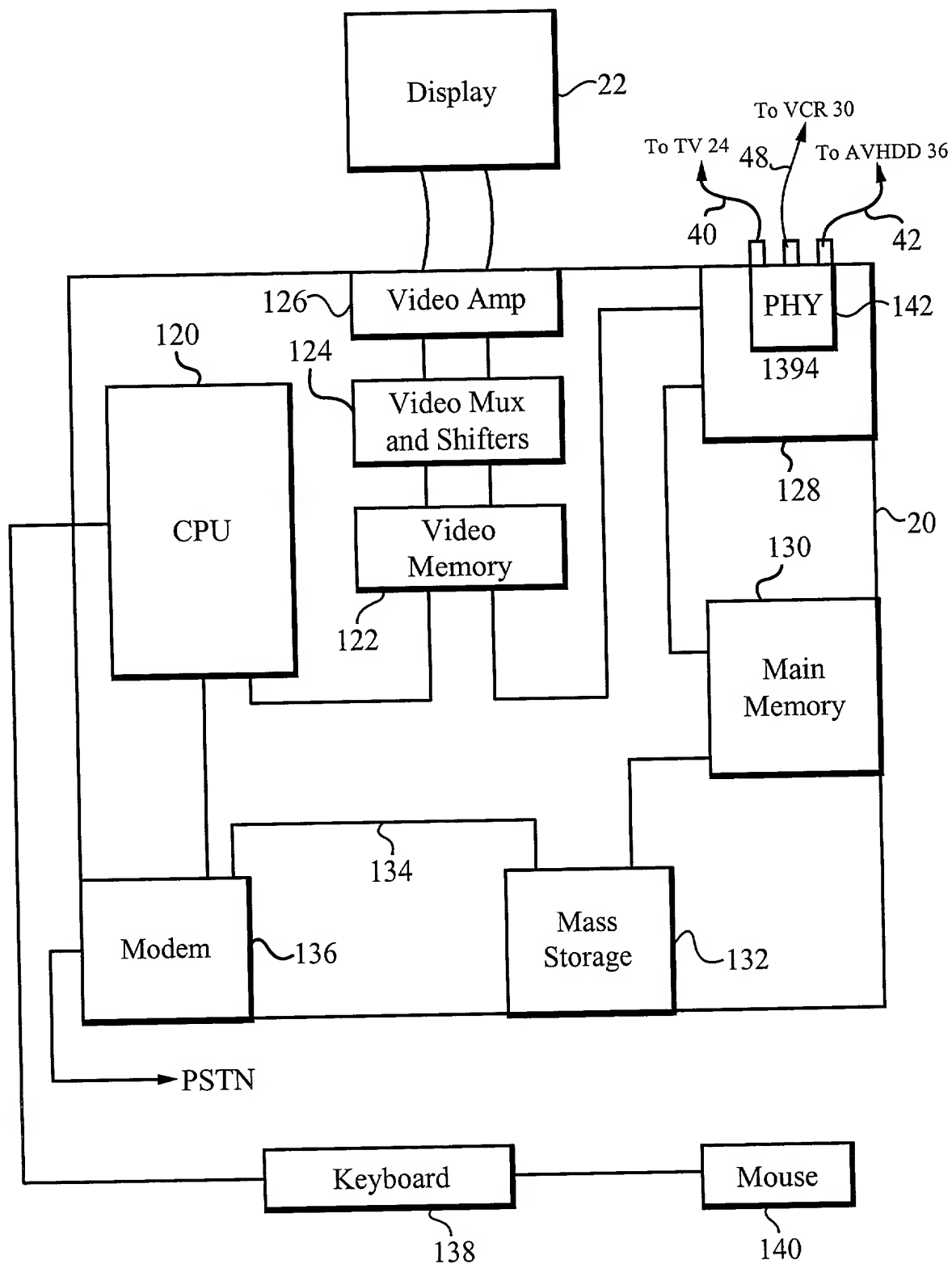


Fig. 2

*Fig. 3*

200

Address	Length, Bytes	Contents							
		7	6	5	4	3	2	1	0
0000 ₁₆	2	Compound_length							
0001 ₁₆									
0002 ₁₆	2	Info_block_type							
0003 ₁₆									
0004 ₁₆	2	Primary_fields_length							
0005 ₁₆									
0006 ₁₆	2	Enable	Bandwidth						
0007 ₁₆		Bandwidth							

Fig. 4

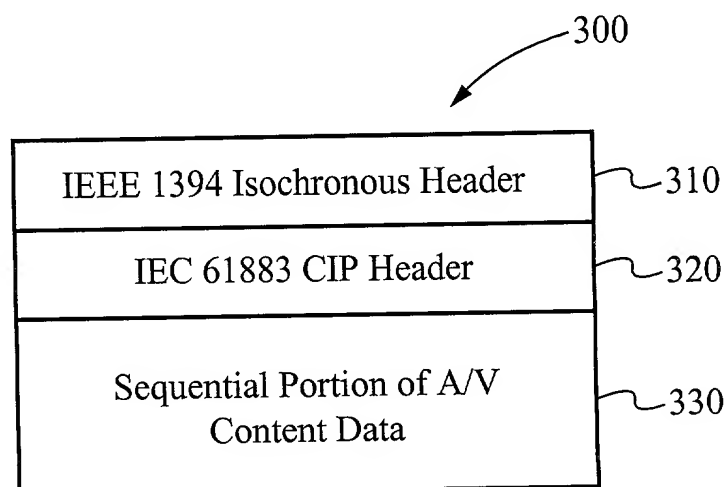


Fig. 5

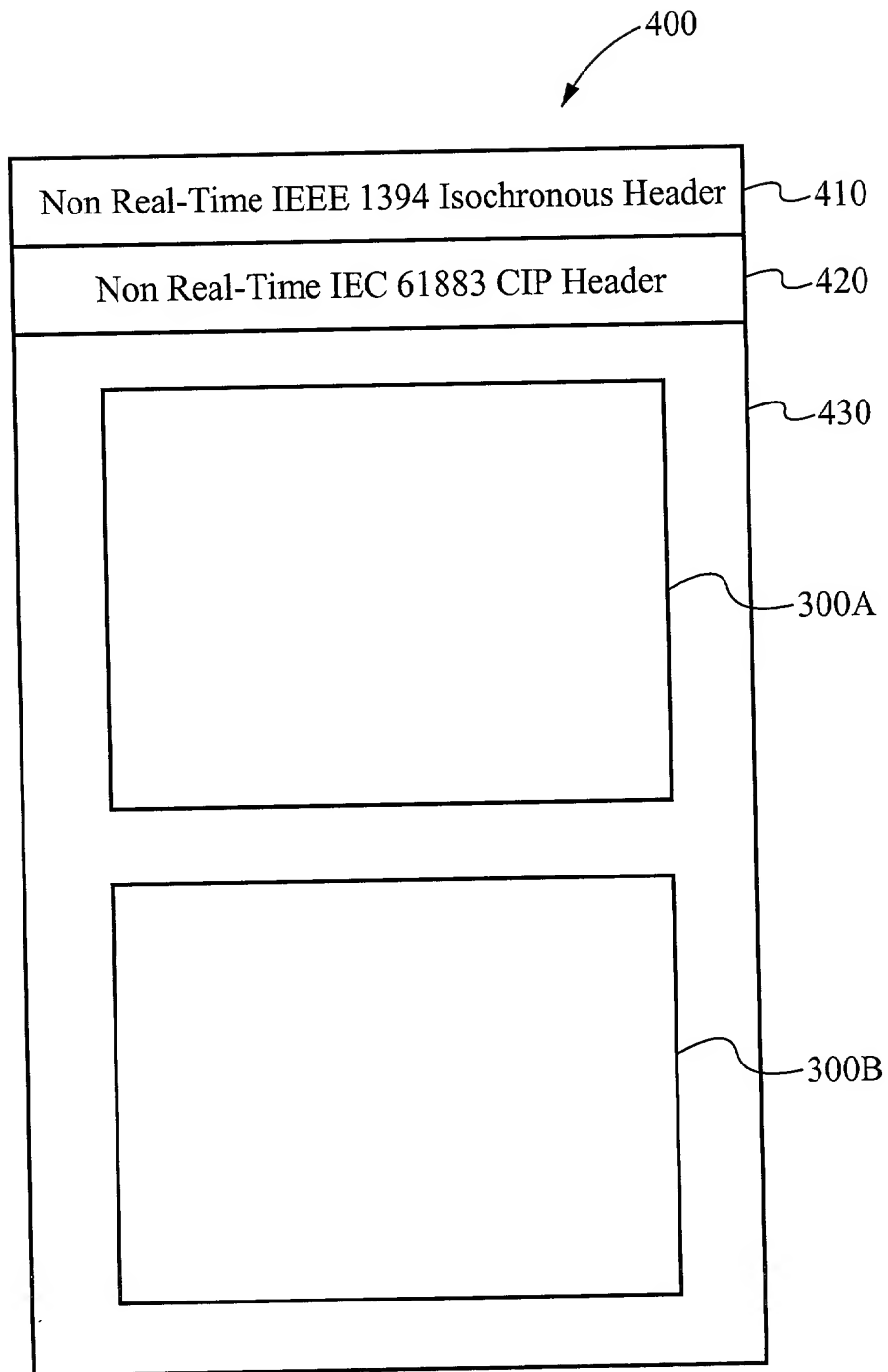


Fig. 6

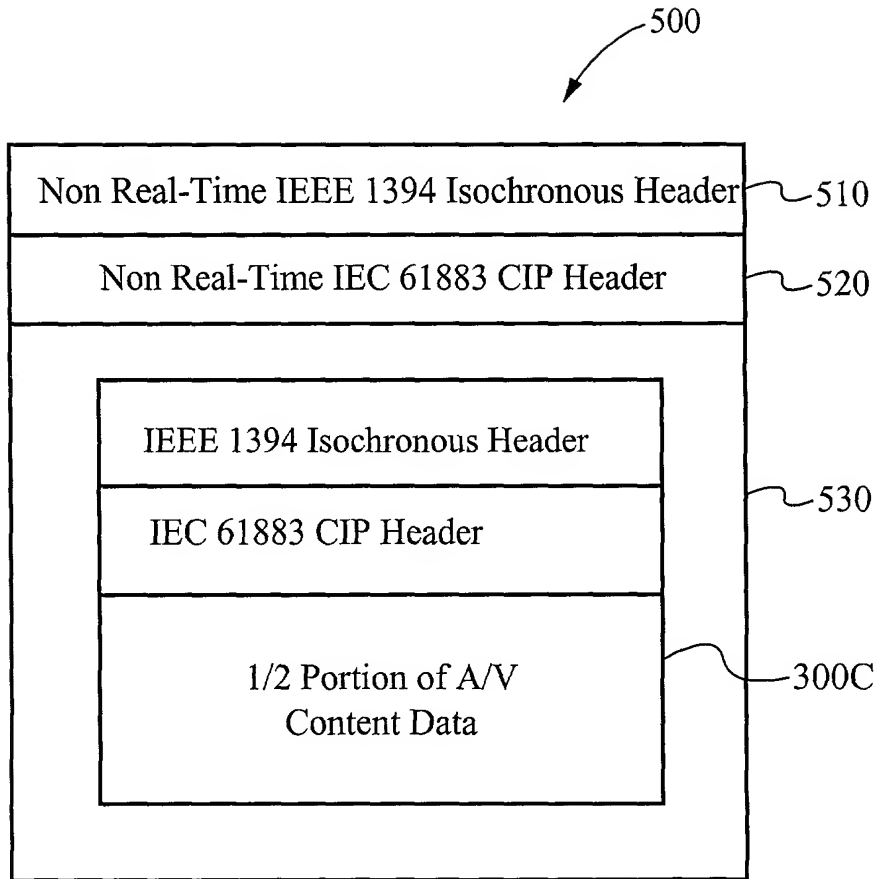


Fig. 7

600

Address	Length, Bytes	Contents							
		7	6	5	4	3	2	1	0
0000 ₁₆	2	Compound_length							
0001 ₁₆									
0002 ₁₆	2	Info_block_type							
0003 ₁₆									
0004 ₁₆	2	Primary_fields_length							
0005 ₁₆									
0006 ₁₆	2	reserved		isochronous_channel_number					

Fig. 8

32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
data_length																tag	channel				tcode				sy							
reserved				dbc				sid				data_channel				control																

Fig. 9

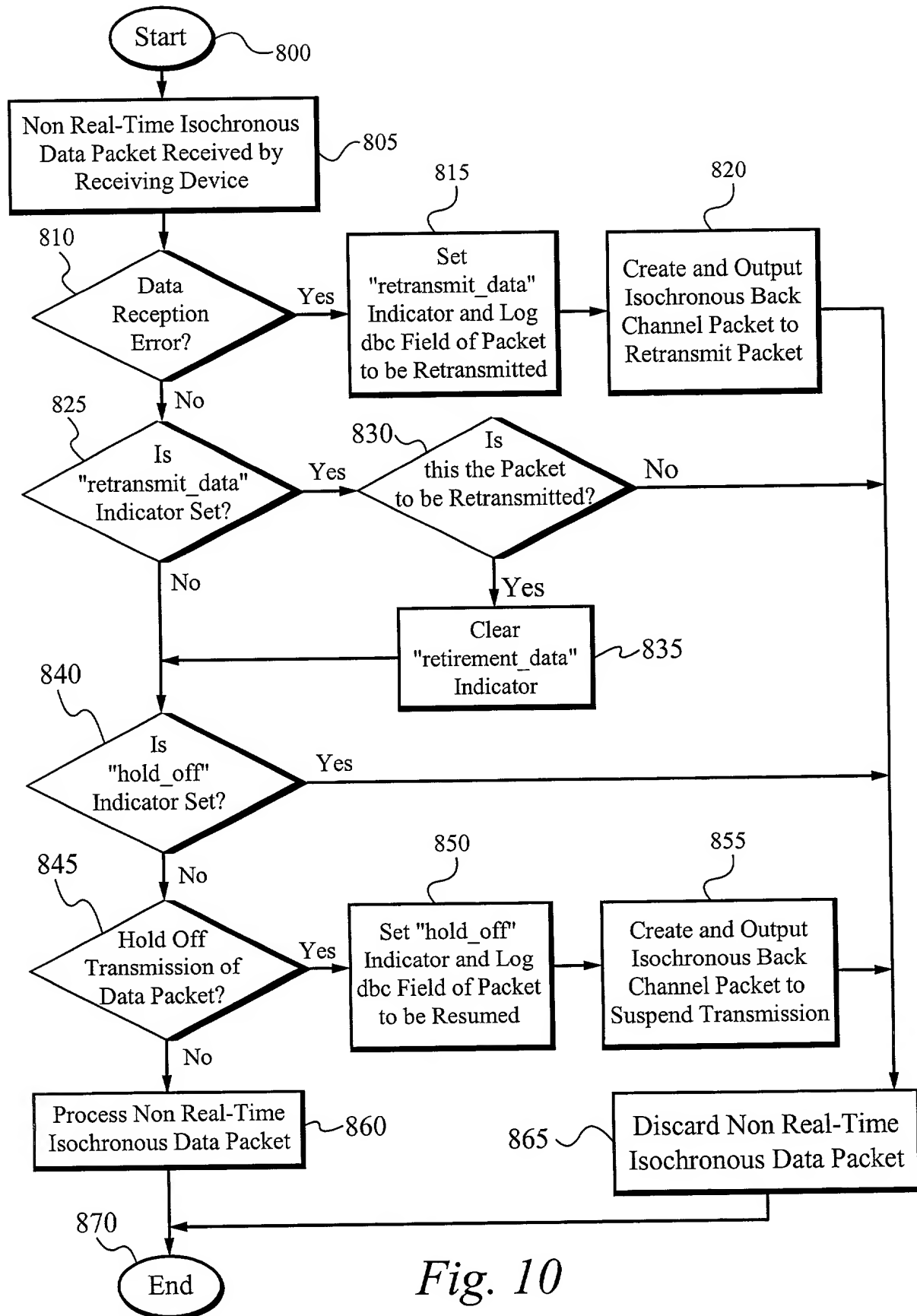
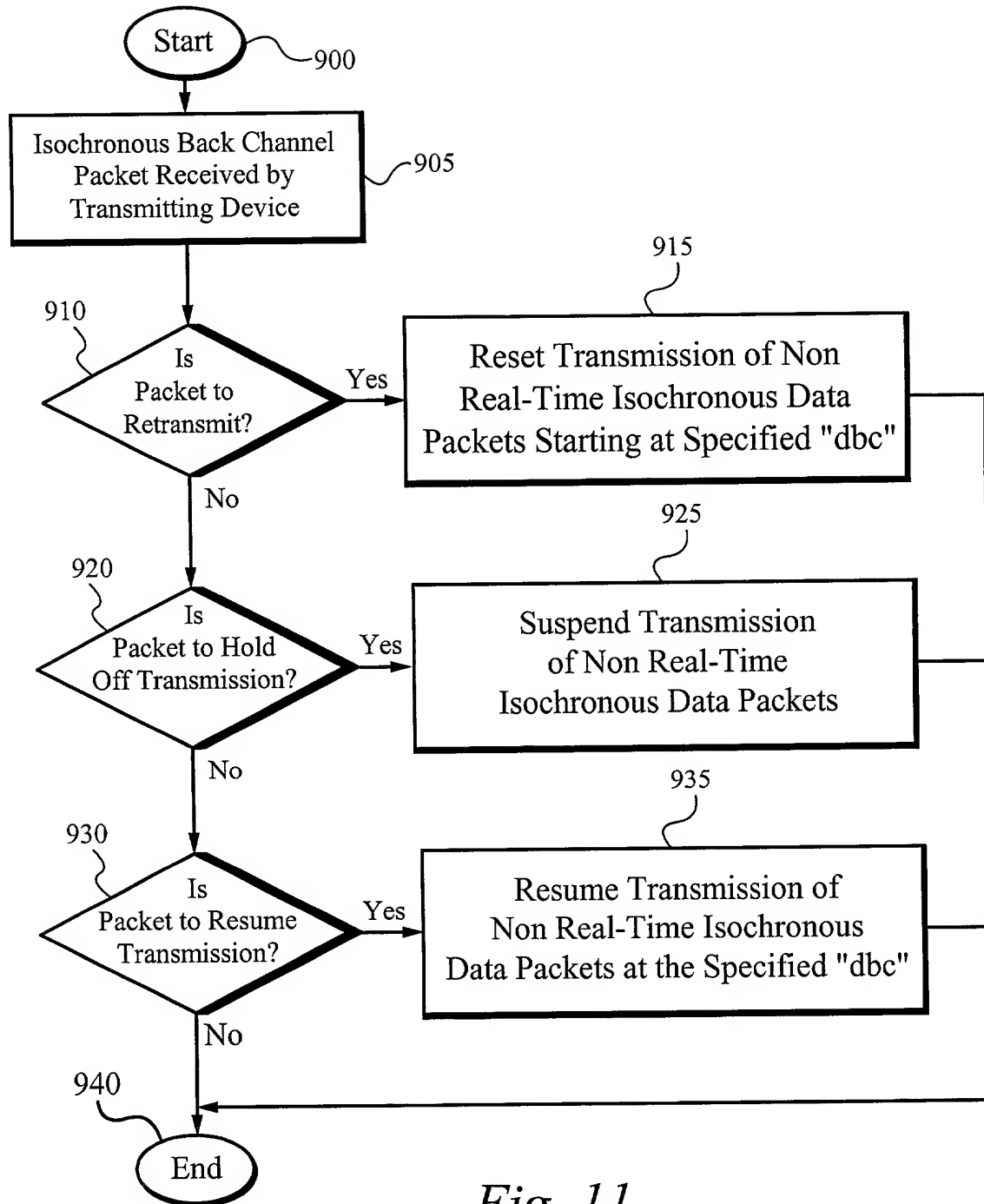
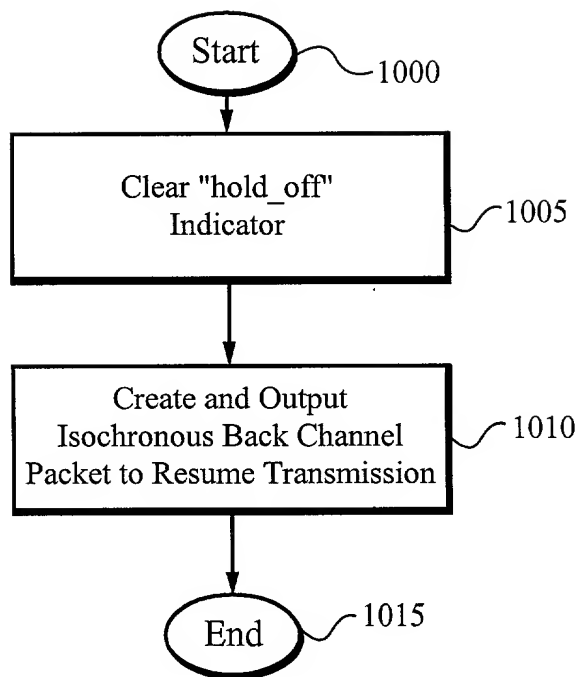


Fig. 10

*Fig. 11*

*Fig. 12*